

**AMENDMENTS TO THE SPECIFICATION**

**Please replace the last paragraph bridging pages 1 and 2 with the following amended paragraph:**

The invention is also related to a devices for thea synchronization of data in an optical WDM transmission system, consisting of the following parts: A wavelength demultiplexer (1) for demultiplexing the incoming data stream in the synchronizer, delay lines (2) for the individual wavelength channels, a multiplexer (100, a modulator (3) modulated by high frequency clock signal (5) and at least one photodetector (4) tapped to output line (B) where the photodetector (4) is connected to an electronic control circuit (6) which is connected to the individual delay lines (2) for an automatically adaption.

**Please replace the last paragraph on page 4 with the following amended paragraph:**

In the first block of Fig. 1, N WDM channels are demultiplexed to allow adjustment of the independently and then re-multiplexed. At this stage a synchronized WDM signal can be used in output A. In the second block, the modulator is driven y an RF signal synchronized with any of the WDM channels (they all operate with the same bit rate) by detecting of the optical signal is extracted by a coupler and detected by a low-frequency photodiode. In a fourth block the detected signal is used by an electronic circuit to control the N time delays of block 1. By using the output B, the scheme of figure 1 is also the main part of a soliton-based regenerator, which also contains amplifiers and pulse reshaping means.

**Please delete the present Abstract of the Disclosure, and add the following new Abstract of the Disclosure:**

The invention is also related to ~~devices and devices~~ for a synchronization of data in an optical WDM transmission system, consisting of the following parts: A wavelength demultiplexer (1) for demultiplexing the incoming data stream in the synchronizer, delay lines (2) for the individual wavelength channels, a multiplexer (10), a modulator (3) modulated by a high frequency clock signal (5) and at least one photodetector (4) tapped to output line (B) where the photodetector (4) is connected to an electronic control circuit (6) which is connected to the individual delay lines (2) for an automatically ~~adaptation~~ delay control.